RRRRRRRRRRR	MMM MMM	SSSSSSSSSS
RRRRRRRRRRR	MMM MMM	SSSSSSSSSS
RRRRRRRRRRR	MMM MMM	SSSSSSSSSS
RRR RRR	MMMMMM MMMMMM	SSS
RRR RRR	MMMMMM MMMMMM	SSS
RRR RRR	ммммм мммммм	SSS
RRR RRR	MMM MMM MMM	SSS
RRR RRR	MMM MMM MMM	SSS
• • • • • • • • • • • • • • • • • • • •		SSS
	MMM MMM MMM	
RRRRRRRRRRR	MMM MMM	SSSSSSSS
RRRRRRRRRRR	MMM MMM	SSSSSSSS
RRRRRRRRRRR	MMM MMM	SSSSSSSS
RRR RRR	MMM MMM	SSS
RRR RRR	MMM MMM	SSS
RRR RRR	MMM MMM	ŠSS
RRR RRR	MMM MMM	ŠŠŠ
RRR RRR	MMM MMM	SSS
RRR RRR	MMM MMM	ŠŠŠ
RRR RRR	MMM MMM	SSSSSSSSSSS
• • • • • • • • • • • • • • • • • • • •		\$\$\$\$\$\$\$\$\$\$\$\$\$
RRR RRR	MMM MMM	\$\$\$\$\$\$\$\$\$\$\$\$

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NWA SETUP AND RELEASE

16-SEP-1984 00:03:18 VAX/VMS Macro V04-00

Page 0

NTONWASET Table of contents (3) (4) (5) 74 100 195 DECLARATIONS
NT\$NWA_INIT, Allocate and Initialize NWA
NT\$NWA_FREE, Deallocate NWA 0000 0000 16-SEP-1984 00:03:18 VAX/VMS Macro V04-00 5-SEP-1984 16:20:56 [RMS.SRC]NTONWASET.MAR:1 Page (1)

0000 \$BEGIN NTONWASET,000,NF\$NETWORK,<NWA SETUP AND RELEASE> 0000 0000 0000 0000 5 ; * 67:* COPYRIGHT (c) 1978, 1980, 1982, 1984 BY DIGITAL EQUIPMENT CORPORATION, MAYNARD, MASSACHUSETTS. 0000 0000 ŎŎŎŎ ALL RIGHTS RESERVED. 0000 THIS SOFTWARE IS FURNISHED UNDER A LICENSE AND MAY BE USED AND COPIED ONLY IN ACCORDANCE WITH THE TERMS OF SUCH LICENSE AND WITH THE INCLUSION OF THE ABOVE COPYRIGHT NOTICE. THIS SOFTWARE OR ANY OTHER COPIES THEREOF MAY NOT BE PROVIDED OR OTHERWISE MADE AVAILABLE TO ANY OTHER DEPONDED. 0000 10 * 0000 11 ; * ŎŎŎŎ * ŎŎŎŎ . * 0000 OTHER PERSON. NO TITLE TO AND OWNERSHIP OF THE SOFTWARE IS HEREBY 0000 • TRANSFERRED. 15 ŎŎČČ 16 17: 0000 THE INFORMATION IN THIS SOFTWARE IS SUBJECT TO CHANGE WITHOUT NOTICE 0000 ; * AND SHOULD NOT BE CONSTRUED AS A COMMITMENT BY DIGITAL EQUIPMENT 18 * 0000 19 CORPORATION. 20 * 21 * 22 * 22 * 25 26 * 26 0000 0000 DIGITAL ASSUMES NO RESPONSIBILITY FOR THE USE OR RELIABILITY OF ITS 0000 SOFTWARE ON EQUIPMENT WHICH IS NOT SUPPLIED BY DIGITAL. 0000 ; * 0000

Page 2 (2)

```
0000
           ; Facility: RMS
0000
ŎŎŎŎ
        31
32
33
0000
              Abstract:
0000
0000
                    This module is responsible for the allocation, initialization, and
0000
        34
                    deallocation of the Network Work Area (NWA) control block.
0000
        35
        36
37
0000
             Environment: VAX/VMS, executive mode
0000
        38
39
0000
                                              Creation Date: 13-JAN-1978
              Author: James A. Krycka,
0000
ŎŎŎŎ
             Modified By:
        40
0000
        41
        42
0000
                    V03-009 RAS02553
                                              Ron Schaefer
                                                                        15-Feb-1984
0000
                             Initialize the $TRNLNM item-list.
0000
        44
0000
        45
                    V03-008 KB(0570
                                              Keith B. Thompson
                                                                        29-Jul-1983
        46
0000
                             Remove ref to ifb$v_nwa
0000
0000
        48
                    V03-007 JAK0104
                                                                22-APR-1983
                                              J A Krycka
                             Modify NT$NWA_INJT and NT$NWA_FREE to handle large DAP buffers
0000
        49
        50
51
0000
                             described by NWASQ_BIGBUF.
0000
        52
53
54
0000
                    V03-006 KRM0059
                                                                20-Aug-1982
                                              K Malik
0000
                             Clear IFB$V_NWA bit and IFB$L_NWA_PTR in NT$NWA_FREE.
0000
        55
0000
                             KRM0055 K Malik 10-Aug-1982 Changed NWA$B_UNDERLINE to FWA$B_UNDER_NOD.
                    V03-005 KRM0055
        56
57
0000
                             Moved old NWAST_NODEBUF & NWAST_QUOTEDBUF initialization
0000
        58
59
                             to RMOFWASET. Initialize NWASB_NODBUFSIZ
0000
0000
0000
        60
                    V02-004 JAK0067
                                                                26-0CT-1981
                                              J A Krycka
                             Initialize NWA$Q_FLG field.
0000
        61
        62
0000
0000
                    V02-003 SPR37751
                                                               10-JUN-1981
                                              J A Krycka
                             If a receive QIO is outstanding, process (stall on) the AST
0000
        64
        65
0000
                             before deallocating the NWA to avoid a BUGCHECK when the AST
        66
0000
                             is delivered.
0000
        68
69
70
71
0000
                    V02-002 JAK0058
                                                                22-MAY-1981
                                              J A Krycka
0000
                             This module was created from code previously residing in
0000
                             RMOXPFN and RMSOCLOSE.
0000
        72
0000
```

Page 3 (3)

0800 8F

3C A9

FFF8'

0008

000B

000F

156

D0

63 50

52

```
NWA SETUP AND RELEASE 10-SEP-1984 00:03:18 VAX/VMS Macro V04-00 NT$NWA_INIT, Allocate and Initialize NWA 5-SEP-1984 16:20:56 [PMS.SRC]NTONWASET.MAR;1
                                                                                                                Page
                              .SBITL NT$NWA_INIT, Allocate and Initialize NWA
      0000
               101
               102
                     NT$NWA_INIT - allocates space for a NWA control block and then initializes
               104
                              selected fields in it and in the FWA.
               105
               106
107
                      Calling Sequence:
               108
                             BSBW
                                       NTSNWA_INIT
               109
                      Input Parameters:
                             R8
R9
                                       FAB address
                                       IFAB address
               114
                             R10
                                       FWA address
               115
                             R11
                                       Impure Area address
               116
               117
                      Implicit Inputs:
               119
      0000
                              None
      0000
               120
121
122
123
124
125
126
127
128
129
                      Output Parameters:
                                       Status code (RMS)
                             R1
                                       Destroyed
                             RŽ
R3
      0000
                                       NWA size in bytes (rounded to page boundary)
      0000
                                       NWA address
      0000
      0000
                      Implicit Outputs:
      0000
                             FWA$Q_NODE
FWA$Q_QUOTED
IFB$L_NWA_PTR
               130
      0000
      0000
               131
      0000
               132
      0000
               133
                             NWA fields
      0000
               134
               135
      0000
                      Completion Codes:
      0000
               136
               137
      0000
                             Standard RMS completion codes
      0000
               138
               139
      0000
                      Side Effects:
      0000
      0000
               141
                             Space is obtained from the RMS buffer manager.
               142
      0000
                   NT$NWA_INIT::
                                                                     : Entry point
               146
               148
                      Allocate space for the Network Work Area (NWA) control block and make IFAB
               149
                      point to it.
               150
      0000
               151
               152
153
154
155
 30
E9
                                       #NWASC_BLN,R2
RMSGETPAG
      0000
                              MOVZWL
                                                                       Specify size of NWA in bytes Allocate space (NOT ZEROED)
      0005
                              BSBW
```

Branch on failure

; Store NWA address in IFAB

(4)

K 16

RO, EXIT

R3, IFB\$L_NWA_PTR(R9)

BLBL

MOVL

	000F 000F 000F	159 :	elected fields in NWA.
	000F 000F 000F 000F	160 161 ASSUME 162 ASSUME 163 ASSUME	NWASQ_XLTBUF2 EQ NWASQ_XLTBUF1+8 NWAST_ITM_ATTR EQ NWAST_ITM_LST NWAST_ITM_SIRING EQ NWAST_ITM_ATTR+12
	000F 000F 000F	164 ASSUME 165 ASSUME 166 ASSUME	NWAST_ITM_STRING_EQ_NWAST_ITM_ATTR+12 NWAST_ITM_MAXINDX_EQ_NWAST_ITM_STRING+12 NWAST_ITM_END_EQ_NWAST_ITM_MAXINDX+12 NWASL_XLTBUFFLG_EQ_NWASL_XLTCNT+4
	000F 63 7C 000F 5: 024C C3 7E 0011 0016	167 168 CLRQ 169 MOVAQ 170	NWA\$Q_FLG(R3) ; Initialize status flags NWA\$Q_XLTBUF1(R3),R1 ; Initialize descriptors for use during
	81 FF 8F 9A 0016 81 02AC C3 9F 001A	171 MOVZBL 172 MOVAB 173 MOVZBL	; logical node name translation #NWA\$C_XLTBUFSIZ,(R1)+ ; Set-up 1st translation buffer NWA\$T_XLTBUF1(R3),(R1)+ ; '' #NWA\$C_XLTBUFSIZ,(R1)+ ; Set-up 2nd translation buffer
	61 03AC C3 9E 0023 026C C3 9E 0028 0248 C3 002C	174 MOVAB 175 MOVAB 176	#NWA\$C_XLTBUFSIZ,(R1)+; Set-up 2nd translation buffer NWA\$T_XLTBUF2(R3),(R1); NWA\$T_ACSBUF(R3),-; Initialize address for access NWA\$Q_ACS+4(R3); control string descriptor NWA\$L_XLTATTR(R3); Initialize node translation attributes
	0228 (3 7(0033	177 CLRL 178 CLRQ 179	NWADL_XLIGNICKS) ; Initialize node translation counter ; and translation buffer flag
81	51 0200 C3 9E 0037 00030004 8F D0 003C 81 0238 C3 9E 0043 81 D4 0048	180 MOVAB 181 MOVL 182 MOVAB 183 CLRL	NWA\$T ITM_LST(R3),R1 ; Initialize \$TRNLNM itemlist #< <lnm\$_attributes@16>!4>,(R1)+ ; Translation attributes NWA\$L_XETATTR(R3),(R1)+ ; Place for attributes</lnm\$_attributes@16>
81	000200FF 8F D0 004A 81 D4 0051	184 MOVL 185 CLRL	(R1)+ ; no return length #< <lnm\$_string@16>!NWA\$C_XLTBUFSIZ>,(R1)+ (R1)+ ; Buffer addr setup later</lnm\$_string@16>
81	00070004 8F DO 0058 81 0234 C3 9E 005F	186 MOVAB 187 MOVL 188 MOVAB	NWA\$L_XLTSIZ(R3),(R1)+ ; Return length #< <lnm\$_max_index@16>!4>,(R1)+ ; Max # of translations NWA\$L_XETMAXINDX(R3),(R1)+ ; Place for count</lnm\$_max_index@16>
	81 7C 0064 0168 C3 94 0066 0170 C3 7C 006A 006E	189 CLRQ 190 CLRB 191 CLRQ	(R1)+ ; No return len and eol NWA\$B_NODBUFSIZ(R3) ; Initialize NODEBUF size field NWA\$Q_BIGBUF(R3) ; Zero big DAP buffer descriptor to
	05 006E	192 193 EXIT: RSB	; indicate that buffer does not exist ; Exit with RMS code in RO

54

```
.SBTTL NT$NWA_FREE, Deallocate NWA
                             196
197
                     006F
                     006F
                     006F
                             198
                                  ; NT$NWA_FREE - deallocates the NWA control block after all receive QIO ASTs
                     006F
                             199
                                           hav been processed.
                     006F
                             200
                             201
202
203
                     006F
                                    Calling Sequence:
                     006F
                     006F
                                           BSBW
                                                    NT$NWA_FREE
                     006F
                             204
                             205
206
207
                     006F
                                    Input Parameters:
                     006F
                                           R8
R9
                     006F
                                                    FAB address
                     006F
                             208
                                                    IFAB address
                     006F
                             209
                                                    IFAB address
                                           R10
                     006F
                             210
                                           R11
                                                    Impure Area address
                     006F
                             211
                             212
213
214
215
216
                     006F
                                    Implicit Inputs:
                     006F
                     006F
                                           IFB$L_NWA_PTR
                                           NWASV_RCVQ10
                     006F
                     006F
                                           NWASV_RCVAST
                             217
218
219
                     006F
                     006F
                                    Output Parameters:
                     006F
                             006F
                                           RO-R5
                                                    Destroyed
                     006F
                                           AP
                                                    Destroyed
                     006F
                     006F
                                    Implicit Outputs:
                     006F
006F
006F
                                           NWA is deallocated
                     006F
006F
006F
                                    Completion Codes:
                                           None
                     006F
                     006F
                             231
                                    Side Effects:
                             232
                     006F
                     006F
                                           Space is returned to the RMS buffer manager.
                             234
                     006F
                             235 :--
                     006F
                     006F
                             236
                     006F
                                 NT$NWA_FREE::
                                                                                 Entry point
Get NWA address
                             237
       3C A9
                     006F
                             238
                                           MOVL
                                                    IFB$L_NWA_PTR(R9),R4
                     0073
                             239
                                           BEQL
                                                                               : Branch if none
                     0075
                             240
                     0075
                             241
                     0075
                             242
243
                                    If a special receive AST is still pending, then stall on it (i.e., wait for it
                     0075
                                    to complete) before deallocating the NWA. A special receive QIO is one posted
                             244
                     0075
                                    by NT$TRANSMIT_PKT that references the NWA and uses NT$STALLAST instead of
                     0075
                                    RMSSTALLAST.
                             246:
                     0075
                             247
                     0075
                             248
249
250
                     0075
                 E1
                                           BBC
                                                    #NWA$V_RCVQIQ,(R4),10$
                                                                                 Branch if special receive not posted
  13 64
                                           BBS #NWASV_RCVAST,(R4),10$
$SETBIT #NWASV_RCVSTALL,(R4)
  OF 64
                     0079
           04
                 ĒΟ
                                                                                 Branch if special received completed
                     007D
                                                                                 Set flag to resume thread after stall
00FC C4
                             251
           59
                 DO
                     0081
                                                    R9, NWASL_THREAD(R4)
                                           MOVL
                                                                               : Save IFAB/IRAB address that we are
```

	NWA SETUP NT\$NWA_FRE	AND RELEASE F, Deallocate NWA	16-SEP-1984 5-SEP-1984	00:03:18 VAX/VMS Macro V04-00 Page 16:20:56 [RMS.SRC]NTONWASET.MAR;1	(5)
0000000°FF	0086 0086 16 0086 0080	252 253 254 JSB 255 256 257	RM\$STALL	<pre>; stalling on for use by NT\$STALLAST ; before it branches to RM\$THREADGO ; Await completion of special receive ; Note: RO contains garbage on return</pre>	
	008C 008C 008C	258; Now it's safe 259;	e to deallocate the NWA	and associated DAP buffers.	
0174 C4 11 55 0170 C4 54 0174 C4 FF61 54 3C A9 55 0800 8F FF55' 3C A9	C08C 008C 008C 0090 0092 0097 0097 0097 0097 00048 00048 00048 00046	260 261 10\$: TSTL 262 263 MOVL 264 MOVL 265 BSBW 266 MOVL 267 15\$: MOVZWL 268 BSBW CLRL 270 271 20\$: RSB	NWASQ_BIGBUF-4(R4) 15% NWASQ_BIGBUF(R4),R5 NWASG_BIGBUF+4(R4),R4 RMSRETPAG IFBSL_NWA_PTR(R9),R4 #NWASC_BLN,R5 RMSRETPAG IFBSL_NWA_PTR(R9)	Branch if big DAP buffers have not been allocated; Get # bytes to return; Get address of buffer area; Deallocate space used by buffer area; Get NWA address; Get # bytes to return; Deallocate space used by NWA; clear in case NWA is deallocated; without deallocating the IFB; Exit with no status in RO	
	OOAF	273 .END		; End of module	

B 1

NT(

V04

16-SEP-1984 00:03:18 VAX/VMS Macro V04-00 5-SEP-1984 16:20:56 [RMS.SRC]NTONWASET.MAR;1 Page 9 (5)

NT(

Psect synopsis!

PSECT name Allocation PSECT No. Attributes ABS 00000000 00 (0.) LCL NOSHR NOEXE NORD NOWRT NOVEC BYTE CON 175.) NF SNETWORK PIC NOWRT NOVEC BYTE 000000AF 01 USR CON REL GBL NOSHR EXE RD SABSS 00000800 2048.) 02 (2.) NOPIC USR CON WRT NOVEC BYTE ABS LCL NOSHR EXE RD

Performance indicators

Phase	Page faults	CPU Time	Elapsed Time
Initialization	32	00:00:00.09	00:00:01.33
Command processing	111	00:00:00.61	00:00:03.53
Pass 1	181	00:00:04.33	00:00:12.99
Symbol table sort	0	00:00:00.44	00:00:00.97
Pass 2	61	00:00:00.96	00:00:03.36
Symbol table output	12	00:00:00.08	00:00:00.08
Psect synopsis output	Ž	00:00:00.02	00:00:00.10
Cross-reference output	Ō	00:00:00.00	00:00:00.00
Assembler run totals	401	00:00:06.54	00:00:22.38

The working set limit was 1200 pages. 20043 bytes (40 pages) of virtual memory were used to buffer the intermediate code. There were 20 pages of symbol table space allocated to hold 334 non-local and 4 local symbols. 273 source lines were read in Pass 1, producing 13 object records in Pass 2. 14 pages of virtual memory were used to define 13 macros.

! Macro library statistics !

Macro library name Macros defined

\$255\$DUA28:[RMS.OBJ]RMS.MLB;1

\$255\$DUA28:[SYSLIB]STARLET.MLB;2

TOTALS (all libraries)

Macros defined

4

5
707ALS (all libraries)

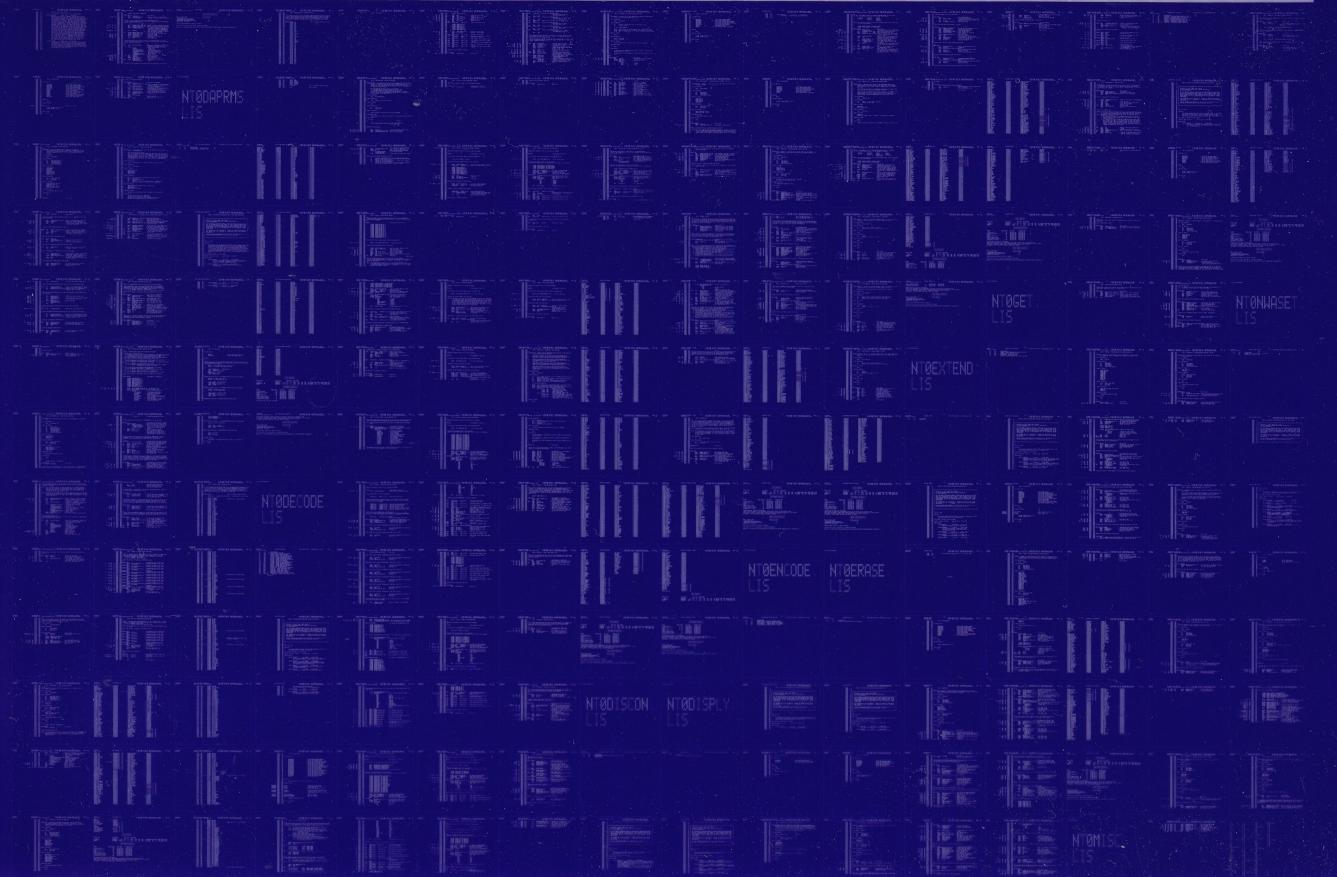
434 GETS were required to define 9 macros.

There were no errors, warnings or information messages.

MACRO/LIS=LIS\$:NTONWASET/OBJ=OBJ\$:NTONWASET MSRC\$:NTONWASET/UPDATE=(ENH\$:NTONWASET)+LIB\$:RMS/LIB

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